

FileViewEditToolsWindowHelp

Links map 1

Pending

Active

L1: (17) US-6340943-S.DID. OR US-6326914-S.DID. OR US-5714955-S.DID. OR US-5107265-S.DID. OR US-...

L2: (642400) transistor

L3: (5) 1 and 2

L4: (16717) channel adj width

L5: (20322) channel adj length

L6: (2250364) ((channel adj width) or w)

L7: (2927656) ((channel adj length) or l)

L8: (376) (identical or similar or equal) near6 ratio near6 6 near5 7

L9: (185) 8 near5 transistor

L10: (0) 9 and 341/144.ccls.

L11: (2464) 8and 341/144.ccls.

L12: (1) 8 and 341/144.ccls.

L13: (14) 8 and 341/15.ccls.

L14: (40) 8 and (dac or (digital adj3 analog))

L15: (25) 9 and (dac or (digital adj3 analog))

L16: (1935) (identical or similar or equal) near6 6 near5 7

L17: (376) 16 near5 transistor

L18: (57) 17 and (dac or (digital adj3 analog))

L19: (15) 16 and display

Failed

(0) (channel adj width) or w)

Saved

Favorites

Tagged (0)

Process

Query

Save

DB: USPAT:US-PG-PUB.EPD.JPO:DERWAT

Display options

OR

Highlight of the items results

16 and display

PDF

HTML

Text

Images

ISAR form

PDF form

	U	I	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval C	Inventor	S	C	P	2	3
1	<input type="checkbox"/>	<input type="checkbox"/>	US 20030231124 A1	20031218	9	Digital-to-analog converting circuit of display	341/136			Yang, Chien-Sheng	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
2	<input type="checkbox"/>	<input type="checkbox"/>	US 20030090402 A1	20030515	47	D/A conversion circuit and semiconductor device	341/154			Nagao, Shou	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
3	<input type="checkbox"/>	<input type="checkbox"/>	US 20020163459 A1	20021107	50	D/A conversion circuit and semiconductor device	341/153			Nagao, Shou	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
4	<input type="checkbox"/>	<input type="checkbox"/>	US RE37944 B	20021231	32	Single chip frame buffer and graphics accelerator	345/519	345/501; 345/546;		Fielder, Dennis A. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
5	<input type="checkbox"/>	<input type="checkbox"/>	US 6496130 B2	20021217	44	D/A conversion circuit and semiconductor device	341/144	341/154		Nagao, Shou	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
6	<input type="checkbox"/>	<input type="checkbox"/>	US 6456270 B1	20020924	25	Integrated circuit device and liquid crystal display	345/98	345/206; 345/92		Itakura, Tetsuro	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
7	<input type="checkbox"/>	<input type="checkbox"/>	US 6380876 B1	20020430	47	D/A conversion circuit and semiconductor device	341/154	341/144		Nagao, Shou	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
8	<input type="checkbox"/>	<input type="checkbox"/>	US 5694143 A	19971202	31	Single chip frame buffer and graphics accelerator	345/519	345/501; 345/545;		Fielder, Dennis et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
9	<input type="checkbox"/>	<input type="checkbox"/>	US 5469195 A	19951121	26	Integrated circuit capacitors, buffers, systems	345/589	257/827.048		Yung, Henry T. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
10	<input type="checkbox"/>	<input type="checkbox"/>	US 5465058 A	19951107	30	Graphics system including an output buffer circuit with	326/83	326/68; 345/600;		Krenik, William R. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
11	<input type="checkbox"/>	<input type="checkbox"/>	US 5365126 A	19941115	31	Graphics system including an output buffer circuit with	326/83	326/68; 327/333;		Krenik, William R. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
12	<input type="checkbox"/>	<input type="checkbox"/>	US 5274284 A	19931228	31	Output buffer circuits with	326/62	326/121		Krenik, William R. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>

Ready

Details

HTML